

VISIT TO SEMI-CONDUCTOR LABORATORY (SCL), CHANDIGARH, INDIA**31st MARCH 2017****TEACHER'S ACTIVITY REPORT 2016 - 2017****FACULTY: SCIENCE****DEPARTMENT/COMMITTEE: ELECTRONICS****IQAC ACTIVITY No: SVC/2016-17/ELECT/NK/3**

NAME OF THE ACTIVITY: VISIT TO SEMI-CONDUCTOR LABORATORY (SCL), CHANDIGARH, INDIA			
DATE	FACULTY	DEPARTMENT/ COMMITTEE	COORDINATOR NAME
31 MARCH, 2017	Science	Electronics	Dr Neeru Kumar Dr Sunita Jain Dr Kunal Srivastava
TIME	VENUE	NUMBER OF PARTICIPANTS	NATURE: OUTDOOR/INDOOR
NA	Chandigarh	08	Outdoor
SUPPORT/ASSISTANCE	Sri Venkateswara College, Visit To Semi-Conductor Laboratory (SCL), Chandigarh, India		

BRIEF INFORMATION ABOUT THE ACTIVITY (CRITERION NO.: III)

TOPIC/SUBJECT OF THE ACTIVITY	Visit to Semi-Conductor Laboratory (SCL), Chandigarh, India
OBJECTIVES	✚ Faculty Improvement Program
METHODOLOGY	✚ Visited the Clean Room Facility ✚ Understood various Fabrication Process Steps such as Dry and Wet Etching, Implantation, Diffusion, and Metallization for 0.8 μ m CMOS Technology ✚ Visited the Dicing, Testing and Packaging Divisions
OUTCOMES	✚ Created a strong understanding about R&D Base in the country in the field of Microelectronics, Design & Development of Devices in Innovative Technology, and Manufacture VLSI / MEMS Based Systems & Sub-systems

PROOFS & DOCUMENTS ATTACHED (Tick mark the proofs attached)

Notice & Letters	Student List of Participation	Activity Report ✓	Photos ✓	Feedback Form
Feedback Analysis	News Clip with details	Certificate	Any Other	

IQAC Document No:	Criterion No: III	Metric No:
Departmental File no	IQAC File No;	

NAME OF TEACHER & SIGNATURE	NAME OF HEAD/ COMMITTEE INCHARGE & SIGNATURE	IQAC COORDINATOR (SEAL & SIGNATURE)
DR NEERU KUMAR	DR NEERU KUMAR	

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For Reference

Criterion I	Curricular Aspects (planning & Implementation)		Criterion V	Student Support & Progression
Criterion II	Teaching Learning & Evaluation		Criterion VI	Governance
Criterion III	Research, Innovations & Extension		Criterion VII	Institutional Values & Best Practices
Criterion IV	Learning Resources and Infrastructure			

PROOFS & DOCUMENTS**Activity Report****Visit to Semi-Conductor Laboratory (SCL), Chandigarh, India****31st March 2017****Faculty Improvement Program**

The faculty members of Department of Electronics visited Semi-Conductor Laboratory (SCL), Chandigarh on 31st March 2017 as a part of faculty improvement program. The laboratory was formerly known as Semiconductor Complex Limited, was established in 1983. The main objective of the lab is to create a strong R&D base in the country in the field of microelectronics, Design & Development of devices in cutting edge technology, and manufacture VLSI / MEMS based systems & sub-systems. SCL has developed 3 micron, 2 micron, 1.2 micron and 0.8 micron CMOS technologies as well specialized technologies such as EEPROM and CCD. The faculty members visited the clean room facility to understand various fabrication process steps such as dry and wet etching, Implantation, Diffusion, and Metallization for 0.8 μm CMOS technology. After that the dicing, testing and packaging divisions were visited. Overall, the experience was enriching and fulfilling.

Photos



SRI VENKATESWARA COLLEGE
(University of Delhi)

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Sri Venkateswara College

IQAC Coordinator

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Dr. Shruti Mathur
Department of Commerce

Dr. Padma Priyadarshini
Department of Sociology

Dr. Nimisha Sinha
Department of Biochemistry

Shri D. Venkat Ramana
A.O(I/C)

This is to certify that the Activity report (Teacher/Department /Society/Association)
has been submitted for documentation to IQAC, Sri Venkateswara College,
University of Delhi.

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